

Please amend the subject application as follows:

IN THE CLAIMS:

Please withdraw claim 49 without prejudice and amend claims 17 and 41 as follows:

1. (original) A liquid crystal display comprising:

a first substrate; and

a plurality of driving signal lines formed on the first substrate, the plurality of driving signal lines including a plurality of voltage transmission lines, wherein each voltage transmission line carries one of a plurality of predetermined voltages and the voltage transmission lines are arranged on the first substrate according to the magnitudes of the predetermined voltages carried by the voltage transmission lines.

2. (original) The display as recited in claim 1, wherein the voltage transmission lines are sequentially arranged based on increasing magnitude of the predetermined voltages carried by the voltage transmission lines.

3. (original) The display as recited in claim 1, wherein the voltage transmission lines are sequentially arranged based on decreasing magnitude of the predetermined voltages carried by the voltage transmission lines.

4. (original) The display as recited in claim 1, wherein the driving signal lines further include a plurality of control signal lines and the plurality of control signal lines is positioned adjacent to the plurality of voltage transmission lines.

5. (original) The display as recited in claim 1, wherein the driving signal lines further include a plurality of control signal lines and the plurality of control signal lines is disposed in between a first voltage transmission line and a second voltage transmission line of the plurality of voltage transmission lines.

6. (original) The display as recited in claim 5, wherein a voltage carried by the control signal lines is equal to the predetermined voltage carried by one of the first and second voltage transmission lines.

7. (original) The display as recited in claim 1, wherein one of the plurality of predetermined voltages is one of a common voltage, a gate-off voltage, a gate-on voltage, a ground voltage, and a supply voltage.

8. (original) The display as recited in claim 1, wherein the driving signal lines further include a plurality of control signal lines and the display further comprises:

a signal controller for generating one of gate control signals and data control signals respectively transmitted via at least one gate control signal line and at least one data control signal line of the plurality of control signal lines;

a common voltage generator for generating a common voltage transmitted via a common voltage transmission line of the plurality of voltage transmission lines; and

a driving voltage generator for generating one of a gate-on voltage and a gate-off voltage respectively transmitted via a gate-on voltage transmission line and a gate-off

voltage transmission line of the plurality of voltage transmission lines.

9. (original) The display as recited in claim 1, further comprising:

a gray voltage generator for generating at least one gray voltage transmitted via a gray voltage transmission line of the plurality of voltage transmission lines.

10. (original) The display as recited in claim 1, wherein the driving signal lines further include a plurality of control signal lines and the display further comprises:

a gate driver including a gate driving integrated circuit for receiving gate control signals transmitted via at least one gate control signal line of the plurality of control signal lines, and for receiving one of a gate-on voltage and a gate-off voltage respectively transmitted via a gate-on voltage transmission line and a gate-off voltage transmission line of the plurality of voltage transmission lines.

11. (original) The display as recited in claim 1, wherein the driving signal lines further include a plurality of control signal lines and the display further comprises:

a data driver including a data driving integrated circuit for receiving data control signals transmitted via at least one data control signal line of the plurality of control signal lines.

12. (original) The display as recited in claim 1, further comprising:

a data driver including a data driving integrated circuit for receiving at least one gray voltage transmitted via a gray voltage transmission line of the plurality of voltage

transmission lines.

13. (original) The display as recited in claim 1, further comprising:

an electrode for receiving a common voltage transmitted via a common voltage transmission line of the plurality of voltage transmission lines.

14. (original) The display as recited in claim 10, wherein the gate driver is disposed on one of the first substrate and a flexible printed circuit film.

15. (original) The display as recited in claim 11, wherein the data driver is disposed on one of the first substrate and a flexible printed circuit film.

16. (original) The display as recited in claim 8, wherein one of the signal controller, the driving voltage generator and the common voltage generator are disposed on a printed circuit board.

17. (currently amended) The display as recited in claim 9, wherein the gray voltage generator is disposed on a printed circuit board.

18. (original) The display as recited in claim 1, further comprising:

a first electrode and a switching element formed on the first substrate, wherein the first electrode is electrically connected to the switching element;

a plurality of display signal lines including at least one gate line and at least one

data line intersecting the at least one gate line, wherein the display signal lines are formed on the first substrate and electrically connected to the switching element;

a second substrate spaced apart from the first substrate by a gap, the gap including liquid crystal; and

a second electrode formed on the second substrate.

19. (original) The display as recited in claim 18, further comprising:

a gate driver including a gate driving integrated circuit for receiving one of a gate-on voltage and a gate-off voltage via respective gate-on and gate-off voltage transmission lines of the plurality of voltage transmission lines, and for transmitting one of the gate-on voltage and the gate-off voltage to the at least one gate line; and

a data driver including a data driving integrated circuit for receiving at least one gray voltage via a gray voltage transmission line of the plurality of voltage transmission lines and for transmitting the at least one gray voltage to the at least one data line.

20. (original) The display as recited in claim 18, wherein the switching element is a thin film transistor.

21. (original) The display as recited in claim 18, further comprising at least one contact assistant connected to an end portion of one of the at least one gate line and the at least one data line.

22. (original) The display as recited in claim 1, wherein at least one voltage

transmission line of the plurality of voltage transmission lines includes at least one pad at an end thereof for defect testing of display signal lines.

23. (original) The display as recited in claim 1, wherein at least one voltage transmission line of the plurality of voltage transmission lines includes at least one pad at an end thereof and a contact assistant connected to the at least one pad.

24. (original) The display as recited in claim 1, further comprising:

a first pad connected to an end of a first voltage transmission line of the plurality of voltage transmission lines, the first voltage transmission line carrying a first voltage of the plurality of predetermined voltages;

a second pad connected to an end of a second voltage transmission line of the plurality of voltage transmission lines, the second voltage transmission line carrying a second voltage of the plurality of predetermined voltages; and

an isolated pad interposed between the first and second pads, wherein the isolated pad is electrically connected to at least one redundant driving signal line and the at least one redundant driving signal line carries a voltage equal to the higher one of the first and second voltages.

25. (original) A liquid crystal display comprising:

a first substrate;

a plurality of control signal lines formed on the first substrate;

a plurality of voltage transmission lines formed on the first substrate, wherein

each voltage transmission line carries one of a plurality of predetermined voltages and the voltage transmission lines are arranged on the first substrate according to the magnitudes of the predetermined voltages carried by the voltage transmission lines;

a switching element formed on the first substrate; and

a plurality of display signal lines including at least one gate line and at least one data line intersecting the at least one gate line, wherein the display signal lines are formed on the first substrate and electrically connected to the switching element.

26. (original) The display as recited in claim 25, wherein the voltage transmission lines are sequentially arranged based on increasing magnitude of the predetermined voltages carried by the voltage transmission lines.

27. (original) The display as recited in claim 25, wherein the voltage transmission lines are sequentially arranged based on decreasing magnitude of the predetermined voltages carried by the voltage transmission lines.

28. (original) The display as recited in claim 25, wherein the plurality of control signal lines is positioned adjacent to the plurality of voltage transmission lines.

29. (original) The display as recited in claim 25, wherein the plurality of control signal lines is disposed in between a first voltage transmission line and a second voltage transmission line of the plurality of voltage transmission lines.

30. (original) The display as recited in claim 29, wherein a voltage carried by the control signal lines is equal to the predetermined voltage carried by one of the first and second voltage transmission lines.

31. (original) The display as recited in claim 25, wherein one of the plurality of predetermined voltages is one of a common voltage, a gate-off voltage, a gate-on voltage, a ground voltage, and a supply voltage.

32. (original) The display as recited in claim 25, further comprising:

a signal controller for generating one of gate control signals and data control signals respectively transmitted via at least one gate control signal line and at least one data control signal line of the plurality of control signal lines;

a common voltage generator for generating a common voltage transmitted via a common voltage transmission line of the plurality of voltage transmission lines; and

a driving voltage generator for generating one of a gate-on voltage and a gate-off voltage respectively transmitted via a gate-on voltage transmission line and a gate-off voltage transmission line of the plurality of voltage transmission lines.

33. (original) The display as recited in claim 25, further comprising:

a gray voltage generator for generating at least one gray voltage transmitted via a gray voltage transmission line of the plurality of voltage transmission lines.

34. (original) The display as recited in claim 25, further comprising:

a gate driver including a gate driving integrated circuit for receiving gate control signals transmitted via at least one gate control signal line of the plurality of control signal lines, and for receiving one of a gate-on voltage and a gate-off voltage respectively transmitted via a gate-on voltage transmission line and a gate-off voltage transmission line of the plurality of voltage transmission lines.

35. (original) The display as recited in claim 25, further comprising:

a data driver including a data driving integrated circuit for receiving data control signals transmitted via at least one data control signal line of the plurality of control signal lines.

36. (original) The display as recited in claim 25, further comprising:

a data driver including a data driving integrated circuit for receiving at least one gray voltage transmitted via a gray voltage transmission line of the plurality of voltage transmission lines.

37. (original) The display as recited in claim 25, further comprising:

an electrode for receiving a common voltage transmitted via a common voltage transmission line of the plurality of voltage transmission lines.

38. (original) The display as recited in claim 25, wherein the gate driver is disposed on one of the first substrate and a flexible printed circuit film.

39. (original) The display as recited in claim 35, wherein the data driver is disposed on one of the first substrate and a flexible printed circuit film.

40. (original) The display as recited in claim 32, wherein one of the signal controller, the driving voltage generator and the common voltage generator are disposed on a printed circuit board.

41. (currently amended) The display as recited in claim 33, wherein the gray voltage generator is disposed on a printed circuit board.

42. (original) The display as recited in claim 1, further comprising:

a first electrode formed on the first substrate, wherein the first electrode is electrically connected to the switching element;

a second substrate spaced apart from the first substrate by a gap, the gap including liquid crystal; and

a second electrode formed on the second substrate.

43. (original) The display as recited in claim 25, further comprising:

a gate driver including a gate driving integrated circuit for receiving one of a gate-on voltage and a gate-off voltage via respective gate-on and gate-off voltage transmission lines of the plurality of voltage transmission lines, and for transmitting one of the gate-on voltage and the gate-off voltage to the at least one gate line; and

a data driver including a data driving integrated circuit for receiving at least one

gray voltage via a gray voltage transmission line of the plurality of voltage transmission lines and for transmitting the at least one gray voltage to the at least one data line.

44. (original) The display as recited in claim 25, wherein the switching element is a thin film transistor.

45. (original) The display as recited in claim 25, further comprising at least one contact assistant connected to an end portion of one of the at least one gate line and the at least one data line.

46. (original) The display as recited in claim 25, wherein at least one voltage transmission line of the plurality of voltage transmission lines includes at least one pad at an end thereof for defect testing one of the at least one gate line and the at least one data line.

47. (original) The display as recited in claim 25, wherein at least one voltage transmission line of the plurality of voltage transmission lines includes at least one pad at an end thereof and a contact assistant connected to the at least one pad.

48. (original) The display as recited in claim 25, further comprising:
a first pad connected to an end of a first voltage transmission line of the plurality of voltage transmission lines, the first voltage transmission line carrying a first voltage of the plurality of predetermined voltages;

a second pad connected to an end of a second voltage transmission line of the plurality of voltage transmission lines, the second voltage transmission line carrying a second voltage of the plurality of predetermined voltages; and

an isolated pad interposed between the first and second pads, wherein the isolated pad is electrically connected to at least one redundant driving signal line and the at least one redundant driving signal line carries a voltage equal to the higher one of the first and second voltages.

49. (withdrawn) An electronic device with conductive lines for transmitting electrical signals comprising:

a substrate; and

a plurality of voltage transmission lines formed on the substrate, wherein each voltage transmission line carries a voltage and the voltage transmission lines are arranged on the substrate according to the magnitudes of the voltages carried by the voltage transmission lines.